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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known

Application Number	09/542,473
Filing Date	April 4, 2000
First Named Inventor	Takayashi IKEDA
Group Art Unit	2826
Examiner Name	A. Sefer
Attorney Docket Number	740756-2138

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Initials ¹	Cite No. ¹	Foreign Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁴
		Office ³	Number ⁴ Kind Code ⁵ (if known)				
A.S.	JP	07-326767		Ikeda	12-12-1995		Abst.

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

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Form PTO-1449 (Rev. 8-82)		Department of Commerce Patent and Trademark Office	Atty Docket 0756-2558	Serial No. 09/542,473
O I P E INFORMATION DISCLOSURE STATEMENT FEB 02 2004 B A L E N T S T H A D E M A R K O F F I C E		Applicants: Takayuki IKEDA		
		Filing Date: April 04, 2000		Group Art. Unit: 2826
U.S. PATENT DOCUMENTS				
Examiner Initial	Document Number	Date	Name	Class
A.S.	6,013,929	01/11/2000	Ohtani	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)				
Examiner Initial				
A.S.	Specifications and Drawings for Application Serial No. 08/862,895, "Semiconductor Intergrated Circuit and Fabrication Method Thereof", Filing Date: May 23, 1997, Inventor: Hisashi OHTANI			
	Specifications and Drawings for Application Serial No. 09/468,859, "Thin Film Transistor, Method of Manufacturing the Same, and Semiconductor Device Including the Same" Filing Date: December 21, 1999, Inventor: Hisashi OHTANI			
	Specifications and Drawings for Application Serial No. 09/487,432, "Semiconductor Device and Process for Production Thereof" Filing Date: January 19, 2000, Inventors: Shunpei YAMAZAKI et al.			
	Specifications and Drawings for Application Serial No. 09/493,411, "Semiconductor Device and Method of Fabricating the Same" Filing Date: January 28, 2000, Inventors: Shunpei YAMAZAKI et al.			
Examiner	A.S.	Date Considered	7/6/04	
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